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PAGE 1/13 \* RCVD AT 4/29/2005 2:29:44 PM [Eastern Daylight Time] \* SVR:USPTO-EFXRF-1/2 \* DNIS:8729306 \* CSID:Haynes and Boone,LLP \* DURATION (mm:ss):03:06

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**REPLY UNDER 37 CFR §1.116**  
**EXPEDITED PROCEDURE**  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:	§	Attorney Docket No.:
Chi-Chun Chen, et al.	§	2002-0066 / 24061.461
Serial No.: 10/600,393	§	Customer No. 42717
Filed: June 20, 2003	§	Group Art Unit: 2822
For: METHOD OF FORMING DUAL	§	Examiner: Toniae M. Thomas
GATE INSULATOR LAYERS FOR	§	
CMOS APPLICATIONS	§	Confirmation No.: 8529

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Kelly A. Moreau  
Kelly A. Moreau

Sir:

**AMENDMENT UNDER 37 C.F.R. §1.116**

This Amendment is submitted in reply to the Office Action mailed on March 25, 2005.  
(Please see the pages that follow).